

Projects that help to Enter in core Company 2021

Projects

Project1: AXI

1. Explanation of AXI theory.
2. Interview question discussion
3. Developing AXI VIP with some of AXI specifications

Project 2 : AHB

1. Explanation of AHB theory.
2. Interview question discussion
3. Developing AHB VIP with some of AHB specifications

Project 3: I2C

1. Explanation of I2C theory.
2. Interview question discussion
3. Developing I2C slave (Verilog) & Verifying it.

Project 4: APB

1. Explanation of APB theory.
2. Interview question discussion
3. Developing APB slave (Verilog) & Verifying it.

Project 5 :UART

1. Explanation of UART theory.
2. Interview question discussion
3. Developing UART slave (Verilog) & Verifying it.

Project 6 :SPI

1. Explanation of SPI theory.
2. Interview question discussion
3. Developing SPI slave (Verilog) & Verifying it.

Project 7 : Router 1X3

1. Explanation of Router theory.
2. Interview question discussion
3. Developing Router DUT (Verilog) & Verifying it.

Project 8: FIFO (synchronous & Asynchronous)

1. Explanation of FIFO theory.
2. Interview question discussion
3. Developing FIFO DUT (Verilog) & Verifying it.

Project 9: Dual Port RAM

1. Explanation of Dual Port RAM.
2. Interview question discussion
3. Verification of Dual Port RAM.

Basic Theory Discussion on following projects (no coding)

- 1.PCie
- 2.DDR
3. Ethernet

