

Semi Design

find you way in #VLSI with us

Physical Design Layout

1. Physical Design Flow
2. Understand the core concepts of Layout
3. Routing
4. Sign off
5. RC Extraction
6. Symbol Making
7. Lambda Rule Specification
8. LVS vs. DRC
9. Floor planning
10. Area Optimization
11. The Placement for Standard cells & macros
12. The Clock Tree Synthesis
13. Perform the complete physical design flow by applying the following the concepts and using the industry standard tools:
14. Setting the environment, logical & physical libraries and technology files
15. The timing constraints, synthesis & Generating Net list
16. The Floor plan with different shapes to reduce congestion & Power consumption
17. The Power network Synthesis
18. The PG Planning- Rings (core), Rings (I/O) Rings (around macros) & stripes
19. The Placement of standard cells & Macros
20. Clock Tree Synthesis and Routing
21. Physical design concepts and setup
22. Design constraints & timing constraints
23. Design & power planning
24. Global placement, CTS, STA and Routing concepts