

# Semi Design

find you way in #VLSI with us

## DESIGN FOR TEST DFT

1. Need for Design for Test
2. ATPG-Common Terminology, ATPG
3. DFT Digital Design
4. Scan ATPG & Fault Simulation
5. Scan Design Rules
6. Scan Test for Transition Delay
7. DFT- BIST Working
8. BIST
9. MBIST, MBIST control modes
10. LBIST
11. Boundary Scan
12. DFT Compiler & Tetramax
13. Overview
14. Flows Supported by DFT Compiler
15. Scan Insertion Flow
16. DFT Compiler Flow and supported Commands
17. Tetramax Flow for ATPG
18. Steps involved in ATPG Process
19. ATPG models targeted by Tetramax
20. Industry standard Project Execution